



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Shawn L. Lloyd et al.

Serial No.: 10/822,572

Filed: April 12, 2004

For: Making Electrical Connections  
Between a Circuit Board and  
an Integrated Circuit

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Art Unit: 2833

Examiner: James R. Harvey

Docket: ITL.1113US  
P18785

Assignee: Intel Corporation

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

Date of Deposit: January 24, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
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